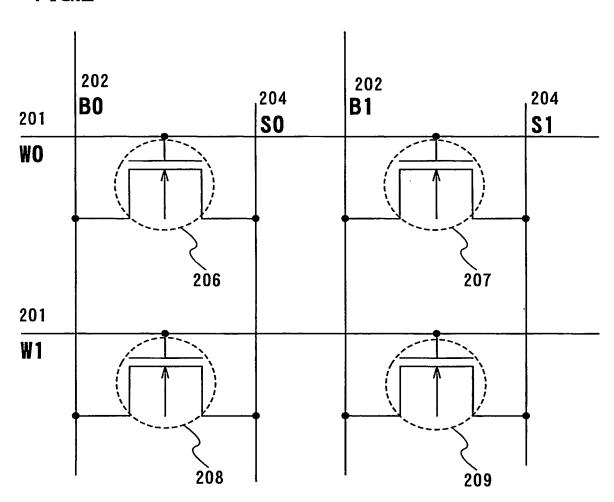


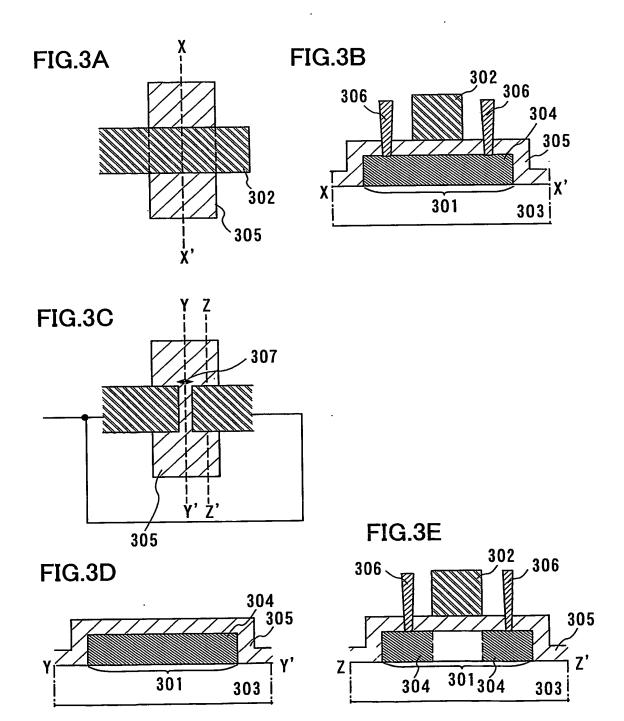
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FIG.2

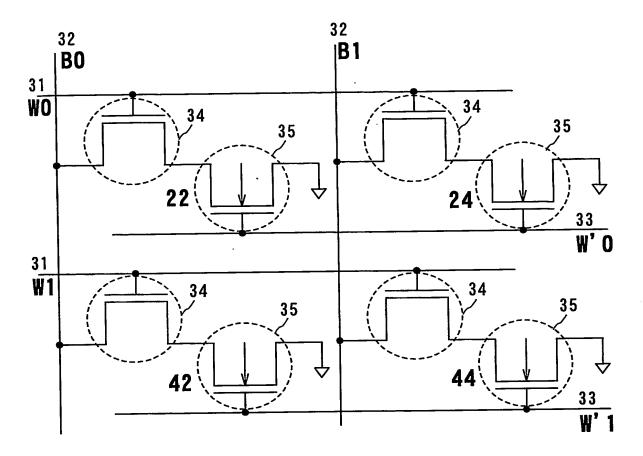


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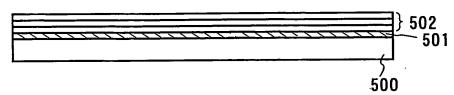
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FIG.4

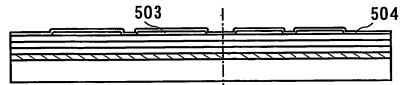


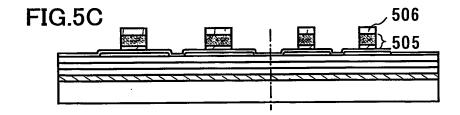
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## FIG.5B





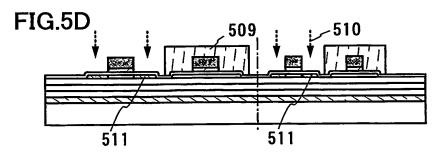


FIG.5E

512

514

514

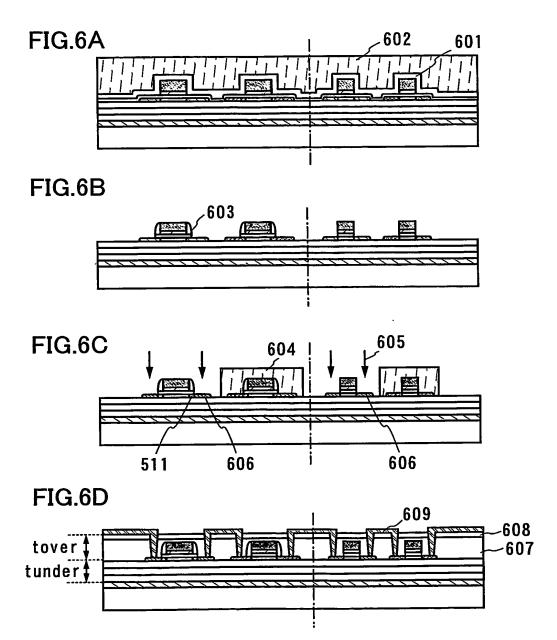
508

507

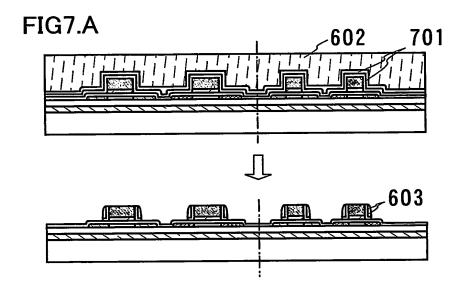
508

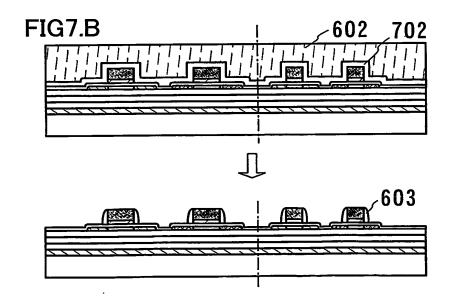
507

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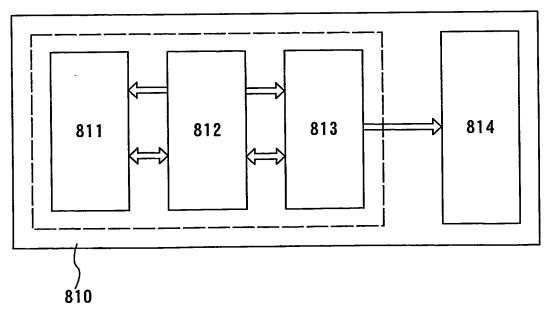
FIG.8A

802
804
806
805

FIG.8B

**8**01

809



807

808

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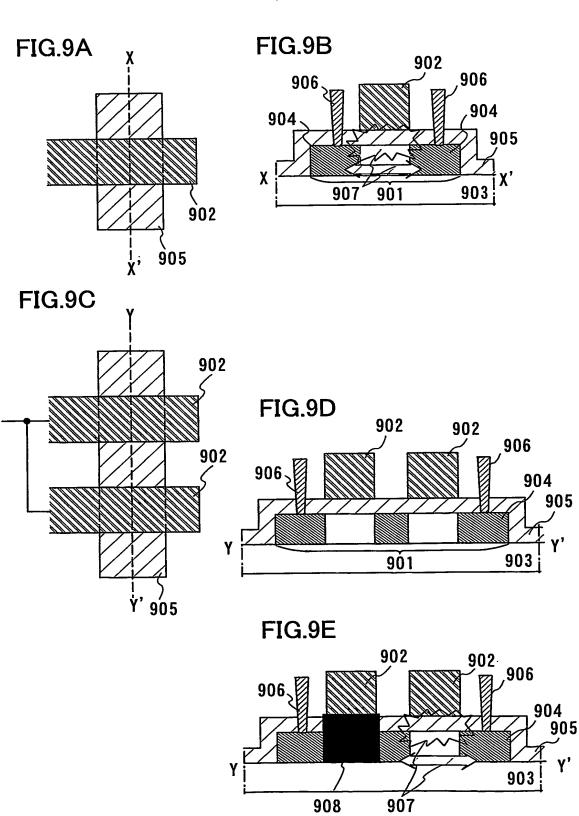


Fig. 10A

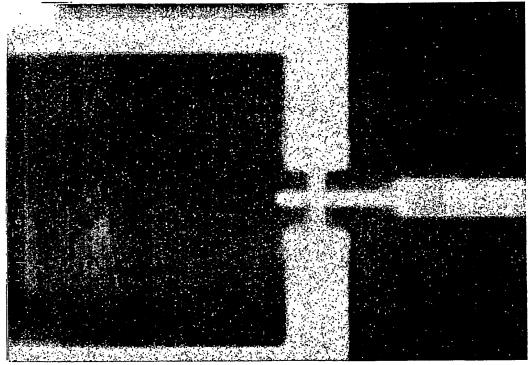
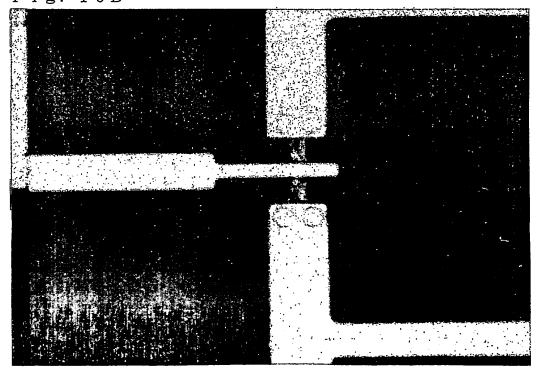


Fig. 10B



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## **EXPLANATION OF REFERENCE**

22: memory cell 24: memory cell 31: word line 32: bit line 33: selection control 34: resistor element 35: selection transistor 42: memory cell 44: memory 100: TFT 101: insulating substrate 102: semiconductor film 103: high cell concentration impurity region 104: channel region 105: gate insulating film 106: gate electrode 107: TFT 108: insulating region 201: word line 202: bit line 203: TFT 204: source line 206: TFT 207: TFT 208: TFT 209: TFT 301: 303: insulating substrate 302: electrode 304: high semiconductor film concentration impurity region 305: insulating film 306: wiring 307: void 500: substrate 501: peeling layer 502: protective film 503: island shape semiconductor 507: film 504: gate insulating film 505: gate electrode 506: resist P-channel TFT 508: N-channel TFT 509: resist 510: impurity element 511: low concentration impurity region 512: resist 513: impurity element 514: high concentration impurity region 601: insulating film 602: resist 603: side wall 604: resist 605: impurity element 606: high concentration impurity region 607: interlayer film 608: protective film 609: wiring 701: insulating film 702: insulating film 801: IC tag 802: RF circuit 803: power supply circuit 804: command control circuit 805: clock 806: congestion control circuit 807: memory 809: antenna 810: electronic appliance 811: 808: memory control circuit memory 812: CPU 813: I/O controller 814: external device 901: semiconductor 903: insulating substrate 904: high concentration 902: gate electrode 905: gate insulating film 906: wiring 907: defect 908: impurity region insulating region

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